

REGISTRATION FORM



ORGANIZING COMMITTEE

TWO DAYS NATIONAL LEVEL WORKSHOP ON

“Semiconductor Technologies, systems and its Applications”

10/03/2025 & 11/03/2025

1. Name :
2. Designation :
3. Educational Qualification:
4. Institution Name:
5. Institution Address:
6. Address for Communication:
7. Email ID :
8. Mobile No :

category

- Faculty
- Research Scholar
- UG/PG Student

Experience (in Years):
Teaching: Research: Industry:

DECLARATION

I hereby declare that the above information is true to the best of my knowledge. I agree to abide by the hands-on training programme and shall attend the course for its entire duration.

Date :
Place:

Signature of the Participant



School of Electrical Engineering Organizing a TWO DAYS NATIONAL LEVEL WORKSHOP on

“Semiconductor Technologies, Systems and their Applications”



Date: 10.03.2025 & 11.03.2025

Venue: ECE Smart Room

RESOURCE PERSONS:

Dr. S.KALPANA
Assistant Professor, IIITDM, Kancheepuram,
Chennai.

Mr. V. DINESH VIJAYARAGAVAN
Suss Microtec, Singapore.

Mr. ARIVAZHAGAN .M
Desgin Verification Engineer, IP & Subsystem
verification (IoT & EDGE AI) eDigiM Research Pvt Ltd.

CHIEF PATRON:

Dr.S.Jagathrakshakan, Founder & Chairman
Dr.J.Sundeeep Aanand, President
Dr.Swetha Sundeeep Anand, Managing Director

PATRON:

Dr.K.Vijaya Bhaskar Raju, Pro Chancellor
Dr.M.Sundararajan, Vice-Chancellor(i/c)
Dr.S.Bhuminathan, Registrar
Dr.R.Hari Prakash, Additional Registrar
Dr.R.M.Suresh, PVC - CoE
Dr.R.Venkatesh Babu, PVC - IR
Dr.M.Sundararaj, Dean-Academics
Dr.P.Naveenchandran, Dean-R&D

CO-PATRONS:

Dr.M.K.Ilampoornan, Dean-IQAC
Dr.S.Prakash, Dean-SEE

CONVENORS:

Dr.B.Kalaiselvi, HoD/ ECE
Dr.S.Lakshmi HoD/EEE

CO-ORDINATORS:

Dr S. Saravana
Associate Professor / ECE

Dr.T.Divya
Assistant Professor / EEE

Ms.S.ArunaMary
Assistant Professor / ECE

Ms. Aashna Unnikrishnan N
Assistant Professor / ECE

ABOUT THE INSTITUTION

Bharath Institute of Higher Education and Research (BIHER), located in Chennai, Tamil Nadu, stands as a testament to the visionary leadership of Dr. S. Jagathrakshakan, its founder. Established in 1984 under the Sri Lakshmi Ammal Educational Trust, BIHER has evolved into a premier deemed-to-be university recognized by the UGC. Dr. S. Jagathrakshakan's mission of delivering quality education and fostering innovation has positioned the institution as a hub for academic and research excellence.

BIHER offers various undergraduate, postgraduate, and doctoral programs across engineering, medicine, law, arts, and management disciplines. Its world-class infrastructure, cutting-edge laboratories, and extensive libraries create an ideal learning environment. With a strong focus on research and industry partnerships, the institute consistently drives technological progress and societal transformation. Accredited by NAAC with an "A" grade, BIHER exemplifies Dr. S. Jagathrakshakan's vision of empowering students to achieve global success and contribute meaningfully to society.

ABOUT THE SCHOOL

The school started in 1992 with two undergraduate programs, adding six post-graduate programs in 2003, along with Ph.D. research opportunities in EEE and ECE. It covers diverse fields like communication systems, wireless networks, VLSI, IoT, renewable energy, and electric mobility. Both B.Tech (EEE & ECE) programs are ABET-accredited.

The school boasts state-of-the-art facilities, dedicated faculty, and advanced labs for research in embedded systems, power electronics, renewable energy, and control systems.

It actively engages in multidisciplinary research and is recognized for excellence in teaching, research, and national service.

The school fosters a competitive environment, driving student success in exams like GATE, CAT, GRE, TOEFL, and PSUs.

The school presents papers at National and International conferences. The school also has a very good industry institute interaction through Consultancy, Training Program, Collaborative Research work, student's project work etc.

SCOPE OF THE WORKSHOP

The workshop aims to provide a comprehensive platform for researchers, industry professionals, and students to explore and discuss the latest advancements in semiconductor technologies and their wide-ranging applications. It seeks to foster collaboration, share innovative ideas, and address the challenges shaping the future of semiconductor systems.

TOPICS TO BE COVERED

DAY 1 (2Hrs)

- ASIC design flow
- Introduction to Verilog
- Introduction to System Verilog

DAY 2 (6Hrs)

- Testbench Architecture
- Data types
- Control flow
- Test bench architecture creation for Flipflop
- Test bench architecture creation for APB

HANDS ON TRAINING BY

Mr.Arivazhagan. M

Requirements:

- Open Wifi Connection without Firewall
- Projector with Board and Marker/Chalk
- System with USB Mouse and USB Keyboard for each batch, if students using laptop plan for Laptop Charging using Extension

Registration Details

Registration Fee : Rs.300/-

Registration Link :

<https://forms.gle/i7RPnCTNc6BPXmGC7>

The registration fees can be paid through UPI

Payment Details:

A/C- NAME	: School of Electrical Engineering
Account Number	: 4557002100003104
IFSC code	: PUNB0455700
Bank Name / Branch	: Punjab National Bank, Selaiyur Branch



For Further details,
Contact:

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Associate Professor / Dept. of ECE

Bharath Institute of Higher Education and
Research, Chennai.

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