

**BHARATH UNIVERSITY**  
**DEPARTMENT OF ELECTRONICS AND INSTRUMENTATION ENGINEERING**

**ODD SEMSTER TIME TABLE / 2017**  
**III-YEAR /V SEMSTER**

DAY /HR	I	II	TEA BREAK	III	IV	LUNCH BREAK	V	VI	VII
MON	DIP	II-1	10.40 - 10.50 AM	MP	DSP	12.30-1.30 PM	PDC	MP	OOPS
TUE	MP	DIP		DSP	PDC		PC LAB		
WED	II-1	PDC		LIB	OOPS		MP LAB		
THU	DSP	MP		OOPS	DIP		II-1	LIB	II-1
FRI	DIP	OOPS		DSP	PDC		OOPS LAB		

**THEORY**

SUB CODE	SUBJECT	FACULTY
BEI501	<i>Digital signal processing [DSP]</i>	Mr.D.SRIDHAR RAJA
BEI502	<i>Process dyanamics &amp; control [PDC]</i>	Ms.B.KALAISELVI
BEI503	<i>Industrial instruementation I [II-1]</i>	Ms.R.ABINETHRI
BEC007	<i>Digital Image Processing [DIP]</i>	Dr.S.LATHA
BEI505	<i>Microprocessor &amp; microcontroller [MP]</i>	Mr.T.VIJAYAN
BEI506	<i>Object oriented programming [OOPS]</i>	Mr.R.KARTHIKEYAN

**PRACTICALS**

SUB CODE	SUBJECT	FACULTY
BEI5L1	<i>Process Control Laboratory [PC LAB]</i>	Ms.B.KALAISELVI
BEI5L2	<i>Microprocessor &amp; dsp lab [MP LAB]</i>	Mr.T.VIJAYAN
BEI5L3	<i>Object oriented programming lab [OOPS]</i>	Mr.R.KARTHIKEYAN

**CLASS INCHARGE**

**H.O.D**

**DEAN**