

**BHARATH UNIVERSITY**  
**DEPARTMENT OF ELECTRONICS AND INSTRUMENTATION ENGINEERING**

**ODD SEMSTER TIME TABLE / 2017**  
**II-YEAR /III SEMSTER**

DAY /HR	I	II	TEA BREAK	III	IV	LUNCH BREAK	V	VI	VII
MON	TE	EM	10.40 - 10.50 AM	CA	M3	12.30-1.30 PM	EMI	DSA	M3
TUE	EMI	M3		LIB	DSA		ME/DSA LAB		
WED	EM	CA		TE	M3		ME/ETE LAB		
THU	TE	CA		DSA	EM		ETE /DSA LAB		
FRI	EMI	EMI		CA	DSA		EM	LIB	TE

**THEORY**

SUB CODE	SUBJECT	FACULTY
<b>BCS301</b>	<i>Data Structures and Algorithm using C++ [DSA]</i>	Ms.R.ABINETHRI
<b>BEE301</b>	<i>Circuit Analysis [CA]</i>	Dr.S.LATHA
<b>BEE302</b>	<i>Electrical Machines [EM]</i>	EEE DEPT /AARTHI
<b>BEI301</b>	<i>Electrical Measurements and Instrumentation [EMI]</i>	Mr.D.SRIDHAR RAJA
<b>BEI302</b>	<i>Transducer Engineering [TE]</i>	Ms.R.ABINETHRI
<b>BMA301</b>	<i>Mathematics - III [M3]</i>	MATHS DEPT

**PRACTICALS**

SUB CODE	SUBJECT	FACULTY
<b>BEI3L1</b>	<i>Measurement and Transducer Engineering lab [ETE LAB]</i>	Mr.D.SRIDHAR RAJA
<b>BEE3L4</b>	<i>Electrical Machines Lab [EM LAB]</i>	EEE DEPT /AARTHI
<b>BCS3L1</b>	<i>Data Structures and Algorithm lab [DSA LAB]</i>	IT DEPT/RAMA PRIYA

**CLASS INCHARGE**

**H.O.D**

**DEAN**