

Faculty Vitae

Name of the Faculty					
Mr.R.S.SIDHARTH RAJ					
Education					
	Degree	Discipline	Institution	Year	
UG	B.E	ECE	Sri Lakshmi Ammal Engineering College	2014	
PG	M.E	VLSI DESIGN	Sri Lakshmi Ammal Engineering College	2016	
Academic experience					
Institution		Title	From	To	FT/PT
Bharath Institute of Higher Education and Research		Assistant Professor	Oct,2016	Till date	FT
Certifications or professional registrations					
Nil					
Membership in professional organizations					
Nil					
Honors and awards					
<ul style="list-style-type: none"> Got medals and cups for playing staff cricket team in various collages Got man of the match award in buhari tournament 2017 					
Service activities (within and outside of the institution)					
<ul style="list-style-type: none"> Going for blood donation camps 					
List of Publications					
<ul style="list-style-type: none"> R.S.Sidharth Raj., Error detection and correction using CDMA , ISSN (Print) : 0974-6846 Indian Journal of Science and Technology, Vol 8(31), DOI: 10.17485/ijst/2015/v8i31/84221, jan 2016. 					
Recent professional development activities					

- Workshop on Network Simulator-NS2, 28th December 2016, Bharath University
- National conference on vlsi design 21st January 2017, Anna university.
- Received certificate of appreciation for coordinating IEEE Conference ICTPACT April 2017
- Received certificate of appreciation for coordinating IEEE Conference ICAMMAET February 2017