

Faculty Vitae

Name of the Faculty					
Ms.M.JASMIN					
Education					
	Degree	Discipline	Institution	Year	
UG	B.E	ECE	PSNA College Of Engineering&Technology Madurai Kamaraj University	2000	
PG	M.Tech	VLSI Design	Bharath University	2009	
PhD	PhD	VLSI	Bharath University	Pursuing	
Academic experience					
Institution		Title	From	To	FT/PT
Bharath University		Assistant Professor	July 2009	Dec 2016	FT
Bharath University		Associate Professor	Jan 2017	Till Date	FT
Certifications or professional registrations					
Nil					
Membership in professional organizations					
Nil					
Honors and awards					
Nil					
Service activities (within and outside of the institution)					
Participated in flood relief campaign in 2015 organized by Bharath University.					
List of Publications					
<ul style="list-style-type: none"> • M.Jasmin, Dr T.Vigneshwaran , S.Beulah Hemalatha, “ Design Of Power Aware On Chip Embedded Memory Based FSM Encoding In FPGA”, International Journal of Applied Engineering Research Vol 10 (2), pp 4487-4496. • M.Jasmin,S.Beulah Hemalatha, “Nanotechnology Based Design For Implementation Of An Arithmetic Structure” , Journal Of Innovative Research And Solutions, Vol 1(2), pp 117-125,2015. • M.Jasmin, “Design of Novel Low power Dual Edge Triggered Flip flop”, Indian Journal Of Science and Technology, Vol 8(32), pp 1-7, DOI: 10.17485/ijst/2015/v8i32/89091,2015. • M. Jasmin, Nelson Pam, Pritam Roy, Dr. D.B.V. Jagannadham,“ An Optimization Engine to Enhance the End User Service” , International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol 4 (5) , pp 4692-4699, DOI: 10.15662/ijareeie.2015.04052012015,2015. • Gulam Dostagir , Jitendra Sharma, Praveen Kumar, Jasmin M, “Design of Efficient Binary Comparators Using Qca Module”,Indian Journal of Research, , Vol 4 (4), pp 10-12, 2015. • S.Beulah Hemalatha, Dr T.Vigneshwaran, M.Jasmin, “Survey on Energy- 					

Efficient Methodologies and Architectures of Network-on-Chip”, Indian Journal Of Science and Technology, Vol 9(12),pp 1-7, DOI: 10.17485/ ijst/ 2016/v9i12/70905, 2016.

- Rajani Kanta Sutar, M.Jasmin, S. Beulah Hemalatha , “Design of Low Power Phase Frequency Detectors and VCO using 45nm CMOS Technology”, International Journal of Science and Research, Vol 4(4), pp 1441-1445.
- Rajani Kanta Sutar, M.Jasmin, S.Beulah Hemalatha, “ Implementation of Low Power All Digital Phase Locked Loop”, International Journal of Innovative Research in Computer and Communication Engineering, Vol 3(4),pp 3733-3739, 10.15680/ijirce.2015.0304151,2015.
- M.Jasmin, “Optimization Techniques for Low Power VLSI Circuits”, Middle-East Journal of Scientific Research, Vol 20 (9), pp 1082-1087 , DOI: 10.5829/idosi.mejsr.2014.20.09.114107 2014.
- M.Jasmin, “Perception Drug case that admonishes correct Dosing”, Middle-East Journal of Scientific Research, Vol 20, pp 1924-1928, 2014.
- Rubiya Yasmin, M.Jasmin, Dr. T.V.U. Kiran Kumar , “ FPGA Implementation of Solid Waste Bin Monitoring and Collection System”, International Journal of Engineering Sciences and Research Technology, Vol 3(10), pp 137-139.
- S.Beulah Hemalatha, Dr T.Vigneshwaran,M.Jasmin , “Design Of NIOS II Soft-Core Based Partial Reconfiguration Controller In FPGA For MpSOC”, International journal of Applied Engineering and Research, Vol 8(32),pp 28571-28579, <http://www.ripublication.com>, 2014
- Rubiya Yasmin, M.Jasmin, Dr. T.V.U. Kiran Kumar, “ Cell Phone Jamming For Safe Driving To Avoid Accidents”, International Journal of Engineering Sciences and Research Technology, Vol 3(8), pp 9-12, 2014.
- M.Jasmin, “A Low Power Pipelined FFT/IFFT Processor for OFDM Applications”, International Journal of Advanced Research in Electrical Electronics and Instrumentation Engineering,, Vol 2 (10), pp 4712- 4721.
- M.Jasmin, “A Concurrent Intellectual Vehicle for Refuge in Embedded Systems” , Middle-East Journal of Scientific Research, Vol 13(12), pp 1551- 1555, DOI: 10.5829/idosi.mejsr.2013.13.12.1230, 2013.
- P.Sathiya, M.Jasmin, “Low Power Compressed Context Architecture Based Processor Design”, International Journal of Advanced Research in Computer Science, Vol 4(4), pp 137-140, 2013.
- M.Jasmin, “ Bus Matrix Synthesis Based On Steiner Graphs For Power Efficient System On Chip Communications”, International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol 1 (2), pp 98- 104, 2012
- M.Jasmin , “Neural Networks For Compensation Of Nonlinearities” , International Journal of P2P Network Trends and Technology, Vol 2(4) , pp 24-27,

2012.

Recent professional development activities

- Presented a paper titled “Fuzzy Controller for Error Control of On Chip Communication” at ICAMMAET,IEEE, Feb, 2017
- Presented a paper titled “An Adaptive Low power Coding Scheme for NOC “ International Conference on ICVESCO at AVIT,CHENNAI,2016
- Presented a paper titled “Design Of Power Aware On Chip Embedded Memory Based FSM Encoding In FPGA”at National Conference On VLSI Design Architecture And Applications-at VIT, Chennai In April 2014.
- Presented a paper titled “Design of Novel Low power Dual Edge Triggered Flip flop” at International Conference at Lakshmi Ammal Engineering College,2014
- Orientation Programme on “The impact of setting up Research Lab in collaboration with Schneider Electricals”, Bharath University,2015
- International Workshop ON “Methodologies On Writing Thesis.Proposals and Journals”, January,2017.
- National Workshop ON Matlab and Simulink - at Bharath University, 2014.
- National Workshop ON Low Power Digital VLSI Design -at VIT Chennai in October 2013.
- Received certificate of appreciation for coordinating IEEE Conference ICTPACT April 2017
- Received certificate of appreciation for coordinating IEEE Conference ICAMMAET February 2017