

Faculty Vitae

Name of the Faculty					
Ms.S.BEULAH HEMALATHA					
Education					
	Degree	Discipline	Institution	Year	
UG	B.E	ECE	Government college of Engineering, Tirunelveli Manonmaniam Sundaranar University	2003	
PG	M.Tech	VLSI Design	Bharath University	2008	
PhD	PhD	VLSI	Bharath University	Pursuing	
Academic experience					
	Institution	Title	From	To	FT/PT
	Bharath University	Assistant Professor	June 2008	Dec 2016	FT
	Bharath University	Associate Professor	Jan 2017	Till date	FT
Certifications or professional registrations					
Nil					
Membership in professional organizations					
Nil					
Honors and awards					
Nil					
Service activities (within and outside of the institution)					
Participated in flood relief campaign organized by Bharath University in 2015					
List of Publications					
<ul style="list-style-type: none"> • S Beulah Hemalatha, T Vigneswaran, M.Jasmin, ,“Survey On Energy-Efficient Methodologies And Architectures Of Network-On-Chip” , Indian Journal of Science and Technology Vol.No:9,issue 12, January 2016. • S.Beulah Hemalatha , T Vigneswaran, M Jasmin, “Design Of Nios II Soft-Core Based Partial Reconfiguration Controller in FPGA For Mpsoc Design” Indian Journal of Science and Technology, Vol.No: 8, Issue 32, November 2015. • M.Jasmin, Dr T.Vigneshwaran , S.Beulah Hemalatha, “ Design Of Power Aware On Chip Embedded Memory Based FSM Encoding In FPGA”, International Journal of Applied Engineering Research Vol.No: 10, Issue 2, pp 4487-4496. • M.Jasmin, S.Beulah Hemalatha, “Nanotechnology Based Design For Implementation Of An Arithmetic Structure” , Journal Of Innovative Research And Solutions, Vol.No: 1, Issue 2, pp 117-125,2015. • D.Sheela, S.Beulah Hemalatha,,” Performance Evaluation of Topology Based Reservation Scheme in WDM Networks”, International Journal of Applied Engineering Research, Vol.No: 9, Issue 22, April 2014. • Satish Kumar, ManjitRay Chowdhury, Maniratnam Kumar, S Beulah Hemalatha, 					

“Brain Controlled Robot for Indian Armed Force”, Indian Journal of Science and Technology in Science and Technology, Vol.No:2,issue2, April 2016.

- Sandeep Kumar Singh, Suman Kumar Singh, Vinay Kumar Pandey, S Beulah Hemalatha, “Real Time Car Parking and Light Control Using Embedded Technology” , International Journal Research, Vol.No:3,issue 8, April 2016.
- S Beulah Hemalatha, “Automated License Plate Recognition”, Indian Journal of Science and Technology ,” Vol.No:8,issue 32, Nov 2015.
- S Beulah Hemalatha, “Vehicle Tracking System Using GPS Tracking Technology” International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, in June 2015.
- Midhila, N.M, S Beulah Hemalatha, “Image Denoising Using Twist Method”, International Organization of Science and Research, in, April 2015.
- RK Sutar, M Jasmin, S Beulah Hemalatha, “Design Of Low Power Phase Frequency Detectors And VCO Using 45nm CMOS Technology” , International journal of Science and Research, Vol.No:4,issue 4, April 2015.
- RK Sutar, M Jasmin, S Beulah Hemalatha, ,“Implementation Of Low Power All Digital Phase Locked Loop”, International Journal of Innovative Research in Computer and Communication Engineering Vol.No:3,issue 4, April 2015.
- S Beulah Hemalatha, “A Delay Buffer For Low Power Using Clock Tree Based Gated Driver”, Elixir, in May 2013.

Recent professional development activities

- Presented a paper titled “Genetic Algorithm Based on Chip Communication Link Reconfiguration for Efficient on Chip Communication” at ICAMMAET, IEEE, Chennai , Feb 2017.
- Presented a paper titled “Design of Nios II Soft-Core Based Partial Reconfiguration Controller in FPGA for Mpsoc Design” at National Conference on VLSI Design Architecture and Applications-at VIT, Chennai In April 2014.
- Presented a paper titled “Reconfigurable Communication Wrapper for QoS Demand for Network on Chip “International Conference on ICVESCOM at AVIT, CHENNAI, 2016.
- Presented a paper at National Conference on Recent Trends in Advanced Computing – at Bharath University, Selaiyur in 2010.
- International Workshop on Methodologies on writing Thesis, Proposals and Journals, at SreeSastha Institute of Engineering and Technology, Chennai in Jan 2017.
- National Workshop on Low Power Digital VLSI Design -at VIT Chennai in

October 2013.

- National Workshop on Matlab and Simulink- at Bharath University, Selaiyur in 2014.
- Faculty Development Programme on NS2 Simulator for Communication, Bharath University, 2016.
- Orientation Programme on “The impact of setting up Research Lab in collaboration with Schneider Electricals”, Bharath University, 2015
- Received certificate of appreciation for coordinating IEEE Conference ICTPACT April 2017
- Received certificate of appreciation for coordinating IEEE Conference ICAMMAET February 2017